

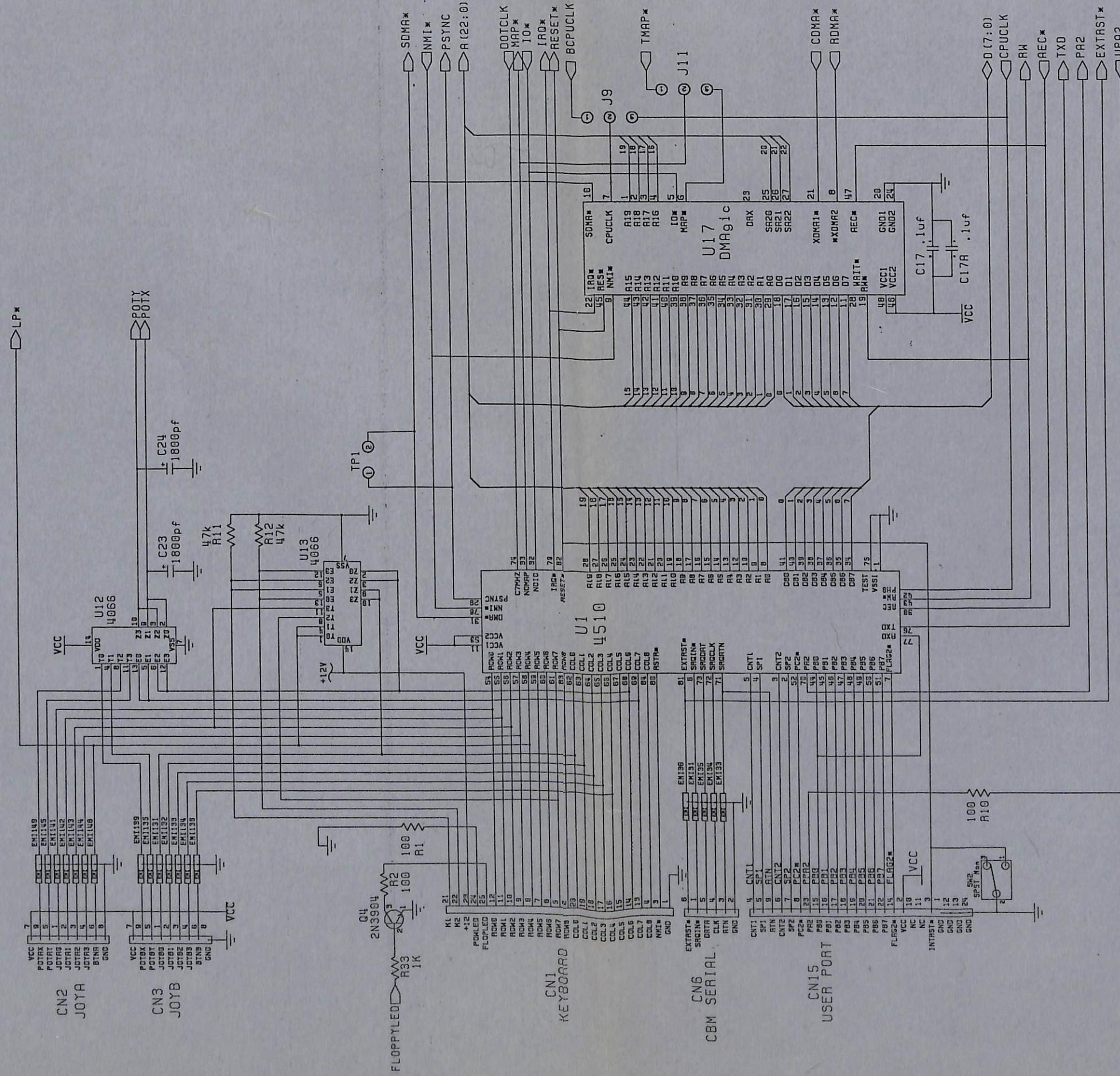
## REVISIONS

LTR	ZONE	DESCRIPTION	DATE	APPROVED
B		REDRAWN & REVISED PER ECO 910309 REV 5 PCB	8-29-91	R. BUCKS

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UNLESS OTHERWISE SPECIFIED				DRAWN BY: <i>G. BERLIN</i>	DATE	commodore	
TOLERANCES ON: DECIMALS				CHKD:			
.X	.XX	.XXX	< 'S	ENGR:			
±	±	±	±	APPR:			
MATERIAL:				USED ON	NEXT ASSY	SCHEMATIC	
				<i>C65</i>	<i>313365</i>	<b>C65 PCB ASSY</b>	
FINISH:				SIZE <b>B</b>		REV <b>B</b>	
				SCALE		SHEET <b>1</b> OF <b>7</b>	

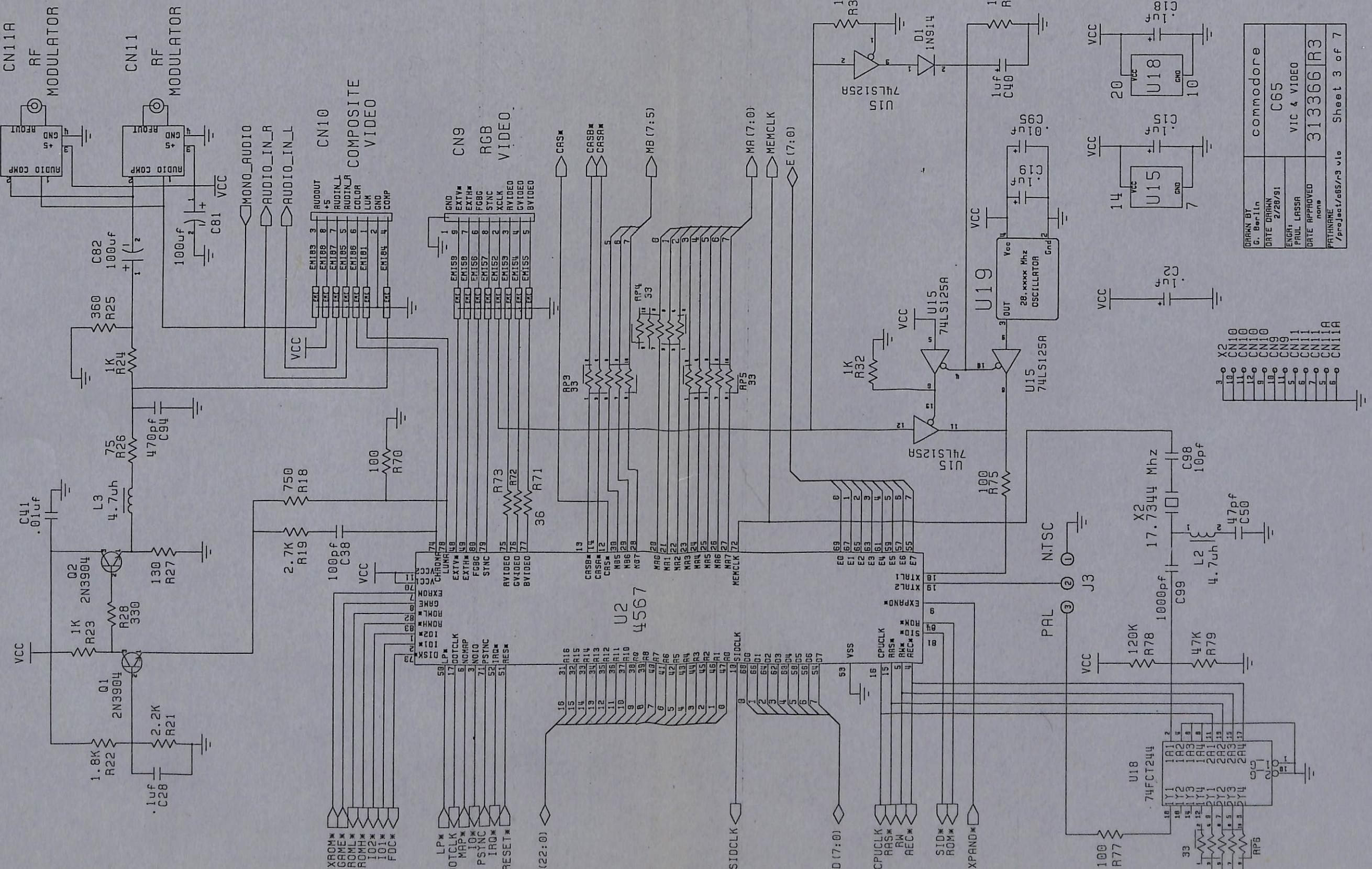


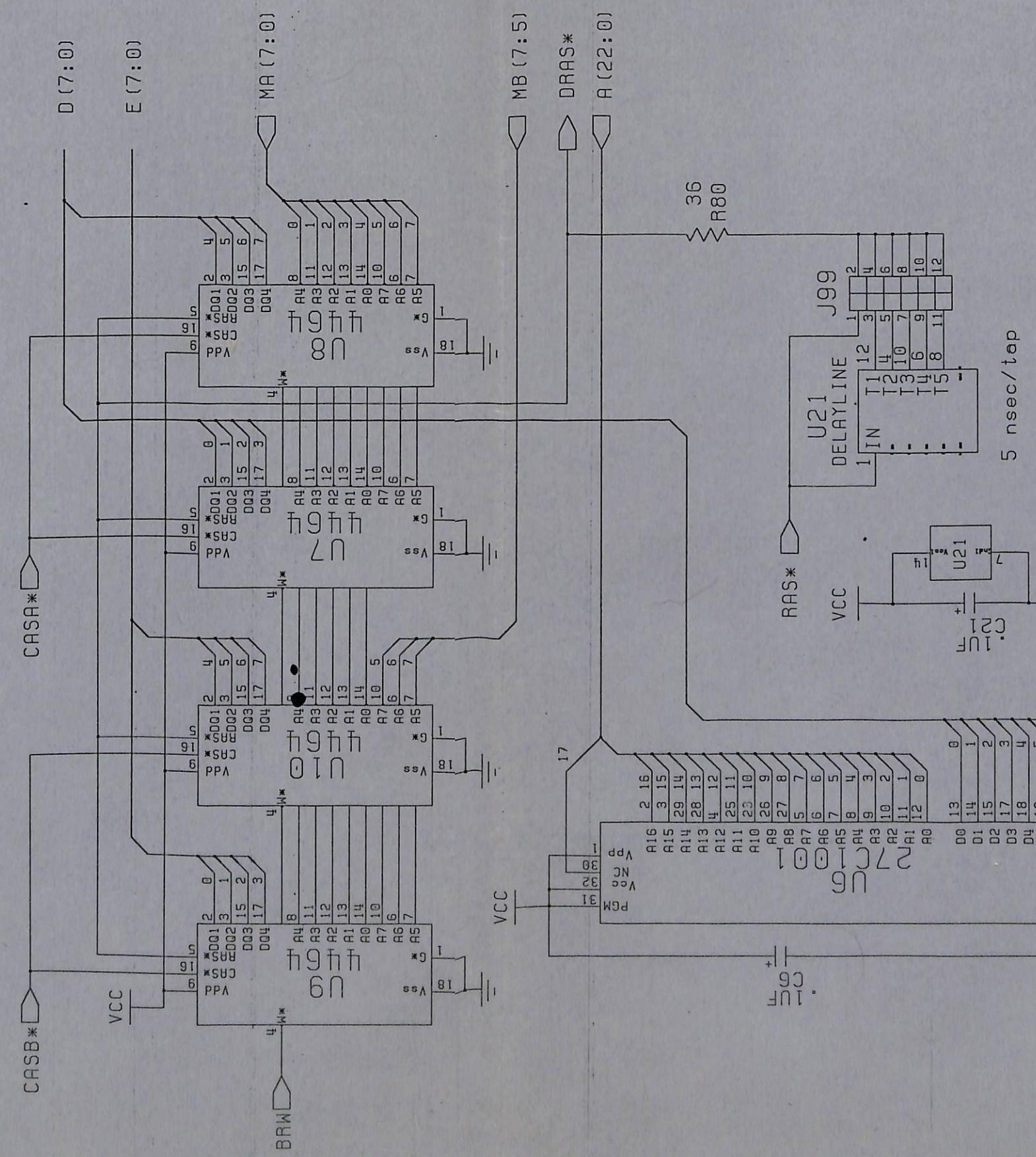
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COMPOSITION	
DEFINITION	C65
VERSION	CPU AND DMA
PUBLIC LOSSER	none
ROUTE APPROVED	313366 R3
ROUTE NUMBER	Sheet 2 of 7
PROJECT/ASSET/CPU	/Project/AS5/r3 cpu

3 7





DRAWN BY G. Berlin	commodore
DATE DRAWN 3/1/91	C65
ENGR: PAUL LASSA	MEMORY
DATE APPROVED none	313366 R3
PATHNAME /project/c65/r3 memory	Sheet 4 of 7

